

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (Previously presented): A digital television system comprising:
a first and second housing;
a receiver, adapted to receive a digital television signal, in said first housing;
a digital television display in said second housing; and
a digital graphics bus coupled to said receiver in said first housing and said display in said second housing.

Claim 2 (original): The system of claim 1 wherein said first housing is part of a modular platform adapted to receive replaceable cards.

Claim 3 (original): The system of claim 2 wherein each of said cards is received in a plug, said plugs for said cards coupled by a bus.

Claim 4 (currently amended): The system of claim 1 wherein said digital graphics bus is coupled to an encryption and a decryption engine so that traffic across said digital graphics bus may be encrypted.

Claim 5 (original): The system of claim 2 wherein one of said cards is a motherboard including a processor.

Claim 6 (original): The system of claim 5 wherein another of said cards is a television tuner/capture card.

Claim 7 (original): The system of claim 6 wherein one of said cards is a digital video disk card.

Claim 8 (original): The system of claim 2 including plugs in said platform for both power and data.

Claim 9 (original): The system of claim 8 wherein said plugs are adapted to receive two different types of serial bus interfaces.

Claim 10 (original): The system of claim 2 wherein said platform includes a processor and an infrared interface.

Claim 11 (currently amended): ~~A digital graphics bus to couple a digital television receiver and a digital television display~~ An apparatus comprising:

a an encryption engine in a digital television receiver coupled to ~~said~~ a digital graphics bus to encrypt signals transferred from said digital television receiver to said digital graphics bus, said encryption engine to provide two different levels of encryption; and

a decryption engine in a digital television display coupled to said digital graphics bus to decrypt signals transferred from said digital graphics bus to said digital television display.

Claim 12 (canceled)

Claim 13 (currently amended): The ~~bus~~ apparatus of claim 11 wherein said digital graphics bus is adapted to periodically encrypt at a higher level of encryption.

Claim 14 (currently amended): The ~~bus~~ apparatus of claim 13 wherein the level of encryption is adapted to change on frame boundaries.

Claim 15 (currently amended): The ~~bus~~ apparatus of claim 11 wherein said encryption and decryption engines include linear feedback shift registers.

Claim 16 (currently amended): The ~~bus~~ apparatus of claim 15 wherein said shift registers include programmable tap registers.

Claim 17 (currently amended): The ~~bus~~ apparatus of claim 16 wherein said programmable tap registers are adapted to receive external tap selection input signals.

Claim 18 (currently amended): The ~~bus~~ apparatus of claim 17 including a combiner adapted to combine a seed signal together with feedback from said programmable tap register to create an input signal to said linear feedback shift register.

Claim 19 (currently amended): The ~~bus~~ apparatus of claim 18 wherein said tap register includes combinatorial logic and tap memory.

Claim 20 (currently amended): The ~~bus~~ apparatus of claim 11 including a decryption and an encryption engine on both ends of said digital graphics bus.

Claim 21 (currently amended): The ~~bus~~ apparatus of claim 11 wherein said digital graphics bus is adapted to transfer streaming video at 100 megahertz or higher.

Claim 22 (original): A modular platform for a digital television system comprising:
a housing including a plurality of slots, each slot including a plug adapted to removably receive a card;

a bus electrically coupling said slots to one another; and

each of said plugs adapted to receive more than one type of serial bus interface.

Claim 23 (previously presented): The platform of claim 22 wherein one of said slots is coupled to receive a motherboard with a processor.

Claim 24 (currently amended): The platform of claim 22 including [[a]] an encryption and decryption engine coupled to an external bus.

Claim 25 (original): The platform of claim 24 wherein said encryption engine is adapted to encrypt at two different levels of encryption.

Claim 26 (previously presented): The platform of claim 25 wherein said encryption levels are changed periodically.

Claim 27 (original): The platform of claim 26 wherein said encryption levels are changed on frame boundaries.

Claim 28 (original): The platform of claim 22 wherein said plugs are adapted to receive both data and power connections.

Claim 29 (original): A method of implementing a digital television system comprising:
providing a receiver in a first housing for receiving a digital television signal;
providing a display in a second housing coupled to said first housing;
transmitting encrypted video signals between said housings; and
periodically changing the level of encryption of said signals.

Claim 30 (original): The method of claim 29 wherein changing the level of encryption includes changing the level of encryption on frame boundaries.

Claim 31 (new): The method of claim 29 further comprising transmitting the encrypted video signals via a digital graphics bus.